

PNS SCHOOL OF ENGINEERING AND TECHNOLOGY, MARSHAGHAI, KENDRAPARA
LESSON PLAN

Branch: Comp. Sc & Engineering. Subject: Pr-2 Digital Electronics Lab.	Sem-3rd No. of days per week: 2 Class Allotted: 60		Name of the teaching faculty: Anjana Tripathy Semestar From Date :15.09.2022 to 22.12.22 No. of weeks : 14
MONTH	WEEK	DATE	PRACTICAL TOPIC
September	1st	16.09.22 19.09.22	Familiarization of Digital Trainer Kit, Logic Pulser Light probes & Digital Ics, IE-7400, 7402, 7404, 7408, 7432 & 7486 (Draw Pin Diagrams)
	2nd	23.09.22	Verify truth table of AND, OR, NOT, NOR, NAND, & OR, X-NOR gates
	3rd	26.09.22	Implement various gates by using universal of NAND & NOR Gates
	3rd	30.09.22	Implement various gates by using universal of NAND & NOR Gates
October	4th	10.10.22	Implement various gates by using universal of NAND & NOR Gates
	5th	14.10.22	Implement half adder and full adder using logic gates
	5th	17.10.22	Construct & verify operation of full adder using logic gates.
	6th	21.10.22	Construct & verify operation of half subtractor using logic gates.
	6th	28.10.22	Construct & verify operation of full subtractor using logic gates.
	7th	31.10.22	Design & Implement a 4-bit Binary to gray Code Converter.
November	7th	04.11.22	Design & Implement a 4-bit Binary to gray Code Converter.
	8th	11.11.22	Design & Implement a single bit Digital Comparator Circuit.
	8th	14.11.22	Design & Implement a two bit Digital Comparator Circuit.
	9th	18.11.22	Design Multiplexer (4 : 1)
	9th	21.11.22	Design De-Multiplexer (1 : 4)
	10th	26.11.22	Study the operation of Flip-Flop, S.R Flip Flop
	10th	28.11.22	Study the operation of J.K. Flip Flop
December	11th	02.12.22	Study the operation of D Flip Flop, T Flip Flop
	11th	05.12.22	Realize a 4-bit a synachronous up/down counter with a control for up/dow counting.
	12th	09.12.22	Realize a 4-bit a synachronous up/down counter with a control for up/dow counting.
	12th	12.12.22	Study Shift Registers
	13th	16.12.22	Verify the operation 8-bit D/A and A/D Conversion & Test.
	14th	19.12.22	Verify the operation 8-bit D/A and A/D Conversion & Test.

Biswanayan Swain

SIGNATURE OF H.O.D

Anjana Tripathy

SIGNATURE OF DEMONSTRATOR