## PNS School of Engg. & Tech, Marshaghai, Kendrapara LESSON PLAN Session (2024-2025)

<b>Discipline:</b> Computer Science		Semester: 4 <sup>th</sup>	Name of the faculty:	
& Engineering			Biswaranjan Swain	
Subject: Data		No. of Days/week: 05	<b>Start Date:</b> 04/02/2025	
Communication & Computer				
Networks (Th2)			<b>End Date: 17</b> /05/2025	
Week	Class Day	Theory Topics		
	1st	1. Network & Protocol		
		Introduction		
	2 <sup>nd</sup>	Data Communication		
1 <sup>st</sup>	3 <sup>rd</sup>	Network characteristics		
	4 <sup>th</sup>	Types of network		
	5 <sup>th</sup>	Protocol & Architecture, Standards		
	1 <sup>st</sup>	OSI model		
	2 <sup>nd</sup>	OSI model		
1	3rd	TCP/IP		
2nd	4 <sup>th</sup>	TCP/IP		
	5 <sup>th</sup>	2. Data Transmission & Media		
		Data transmission Concepts ar	nd Terminology	
	1 <sup>st</sup>	Analog transmission		
	2 <sup>nd</sup>	Digital Data transmission		
3rd	3rd	Digital Data transmission		
	4 <sup>th</sup>	Transmission impairments, Channel capacity		
	5 <sup>th</sup>	Transmission media, Guided Transmission		
	1st	Guided Transmission		
	2nd	Wireless Transmission		
	3rd	3. Data Encoding		
4th		Introduction to encoding tech	niques	

	4 <sup>th</sup>	Digital data to digital signals	
	5 <sup>th</sup>	Line coding,Uni-polar , polar, bipolar	
5th	1 <sup>st</sup>	Block coding and scrambling	
	2nd	Digital data to analog signals, Amplitude shift keying (ask)	
	3rd	Frequency shift keying (fsk), Phase shift keying (psk)	
	4 <sup>th</sup>	Analog data to digital signals,	
	5 <sup>th</sup>	Sampling, Quantization, Encoding	
	1 <sup>st</sup>	Analog data to analog conversion, Amplitude modulation	
	2nd	Frequency modulation, Phase modulation	
6 <sup>th</sup>	3rd	4.Data Communication & Data link control	
		Asynchronous and Synchronous Transmission	
	4 <sup>th</sup>	Error detection, Simple parity check,	
	5 <sup>th</sup>	Two-dimensional parity check, Check sum	
7th	1 <sup>st</sup>	Cyclic redundancy check, Line configuration	
	2nd	Error correction (hamming code),Flow control	
	3rd	Stop and wait, Sliding window, Error control, Stop and wait arq	
	4 <sup>th</sup>	Two-dimensional parity check, Check sum	
	5 <sup>th</sup>	Multiplexing	
8 <sup>th</sup>	1 <sup>st</sup>	FDM synchronous TDM,	
	2 <sup>nd</sup>	Statistical TDM	
	3rd	5. Introduction to Switching & Routing Introduction to switching techniques, Circuit switching	
	4 <sup>th</sup>	Packet switching, Datagram packet switching	
	5 <sup>th</sup>	Virtual circuit switching	
	1st	X.25 protocol, X.25 structure, Layer of x.25	
9th	2 <sup>nd</sup>	Routing in packet switching network	
	3rd	Introduction to congestion, Effects of congestion	
	4 <sup>th</sup>	Congestion control, Open loop & close loop, Traffic	
	5 <sup>th</sup>	Congestion control in packet switching	

10 <sup>th</sup>	1 <sup>st</sup>	6. LAN Technology	
		Introduction to topology & various types of topologies	
	2 <sup>nd</sup>	LAN architecture,	
	3rd	Medium access control	
	4 <sup>th</sup>	Network devices, Repeater, Hub, Bridge	
	5 <sup>th</sup>	Switch, Router, Gateway,	
11th	1 <sup>st</sup>	Ethernet, Types of ethernet networks	
	2 <sup>nd</sup>	CSMA,CSMA/CA,	
	3rd	CSMA/CD	
	4 <sup>th</sup>	Fiber channel, Wireless LAN Technology	
	5 <sup>th</sup>	7. TCP/IP	
		TCP/IP Protocol Suite,Basic Protocol functions	
	1 <sup>st</sup>	Principles of Internetworking	
12 <sup>th</sup>	2 <sup>nd</sup>	Internet Protocol operations	
	3rd	Internet Protocol operations	
	4 <sup>th</sup>	Internet protocol -ip addressing	
	5 <sup>th</sup>	Internet protocol-ip sevices &	

Biswaranjan Swain

SIGNATURE OF LECTURER

Biswaranjan Swain

SIGNATURE OF H.O.D