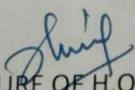
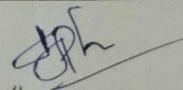
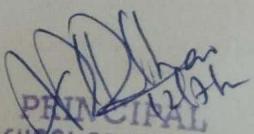


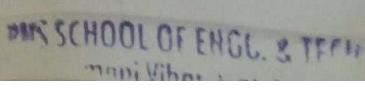
PNS SCHOOL OF ENGINEERING AND TECHNOLOGY, MARSHAGHAI, KENDRAPARA
LESSON PLAN

Branch: Electrical Subject: Pr-3 DEM LAB	Sem-5th No. of days per week: 2 Class Allotted: 45	Name of the teaching faculty: Anjana Tripathy Semestar From Date : 01.07.2025 to 15.11.2025 No. of weeks : 14
MONTH	WEEK	PRATICAL TOPIC
July	1st	Verify truth table of AND, OR, NOT, NOR, NAND, & OR, X-NOR gates
	2nd	Impliment verious gates by using universal of NAND & NOR Gates
	3rd	Verify truth table
	3rd	Impliment half adder and full adder using logic gates
August	4th	Impliment half subtractor and full subtractor using logic gates
	4th	Impliment a 4-bit Binary to gray code converter
	5th	Impliment a single bit digital computer
	5th	Study multiplxer and demultiplexor
	6th	Realize a 4-bit a synchronous UPI Down countre with a control for UPI Down
	6th	Study of Flip-Flop
September	7th	S-R flip Flop (ii) J.K Filp flop (iii)filp flop (iv) T flip flop
	7th	Impliment mode -10 a synchronous counters
	8th	Study shift register
	8th	1'S compliment & 2'S compliment
October	9th	Addition of 8 bit no. (decimal &hexadecimal)
	9th	Subtraction of 8 bit number hexadecimal
	10th	Subtraction of 8 bit number hexadecimal
	10th	Compare between two no. find the largest number
	11th	Compare between two no. find the smallest number
November	11th	Block transfer
	12th	Traffic light control using 8255
	12th	Traffic light control using 8255
	13th	Generation of square wave using 8255
	14th	Revision
	14th	Revision


 SIGNATURE OF H.O.D.


 SIGNATURE OF DEMONSTRATOR


 PRINCIPAL
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